

WHAT IS CLAIMED IS:

1 1. A method for emulation communications via a test
2 access port and boundary-scan architecture providing serial
3 access to a serial connection of a plurality of registers
4 disposed in a plurality of modules, comprising the steps of:
5 selecting for communication one of said plurality of
6 modules, nonselected module being nonresponsive to data on
7 said serial connection;
8 supplying to the test access port for communication to
9 the boundary-scan architecture a serial signal having a first
10 logic state for a number of cycles greater in number than a
11 number of bits of the serial connection of the plurality of
12 registers;
13 following supply of said serial signal, supplying to the
14 test access port for communication to the boundary-scan
15 architecture a start bit having a second logic state opposite
16 to said first logic state followed by a predetermined number
17 of data bits;
18 at said selected module detecting said start bit and
19 storing said predetermined number of data bits.

1 2. The method of claim 1, wherein:
2 said step of storing said predetermined number of data
3 bits consists of storing said predetermined number of data
4 bits in a program visible data register.

1 3. The method of claim 1, further comprising:
2 at said selected module, interpreting said predetermined
3 number of data bits as an instruction and performing a
4 function corresponding to said instruction.

- 1 4. The method of claim 1, further comprising:
2 at said selected module, supplying a serial signal having
3 ~~said first logic state to following registers in the serial~~
4 ~~connection of the plurality of registers during a first time~~
5 ~~interval and supplying to following registers in the serial~~
6 ~~connection of the plurality of registers a start bit having a~~
7 ~~second logic state opposite to said first logic state followed~~
8 by said predetermined number of data bits.

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